

Digitally Tuned Low Power Gyroscope

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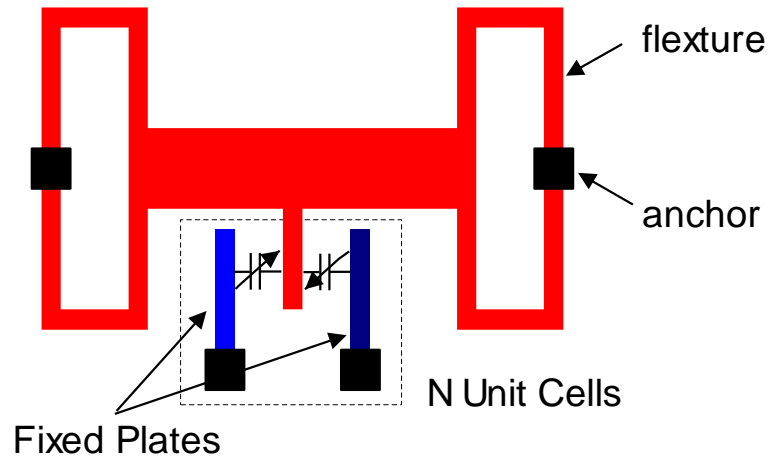
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Outline

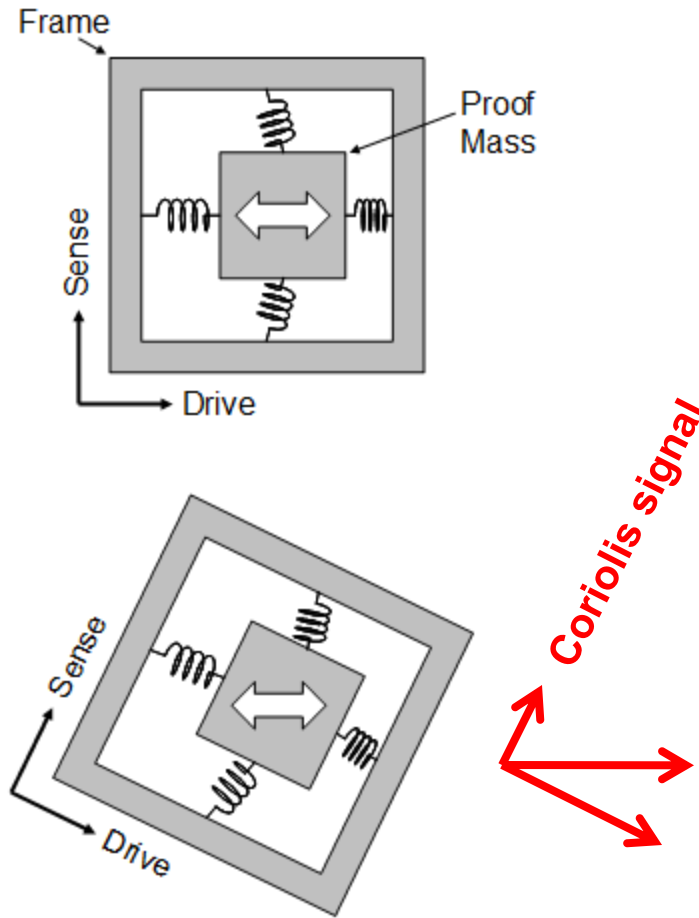
- Objective:
 - 100x power reduction in MEMS gyroscope
- What are gyroscopes?
- Power reduction techniques
 - Mechanical gain
 - Low power, low noise amplification
- Results

Accelerometer



$$\begin{aligned}x &= \frac{a}{\omega^2} \\ &= \frac{1\text{mG}}{(2\pi \times 10\text{kHz})^2} \\ &= 2.5\text{pm} \\ &= \frac{1}{40} \text{Angstrom}\end{aligned}$$

Vibratory Gyroscope



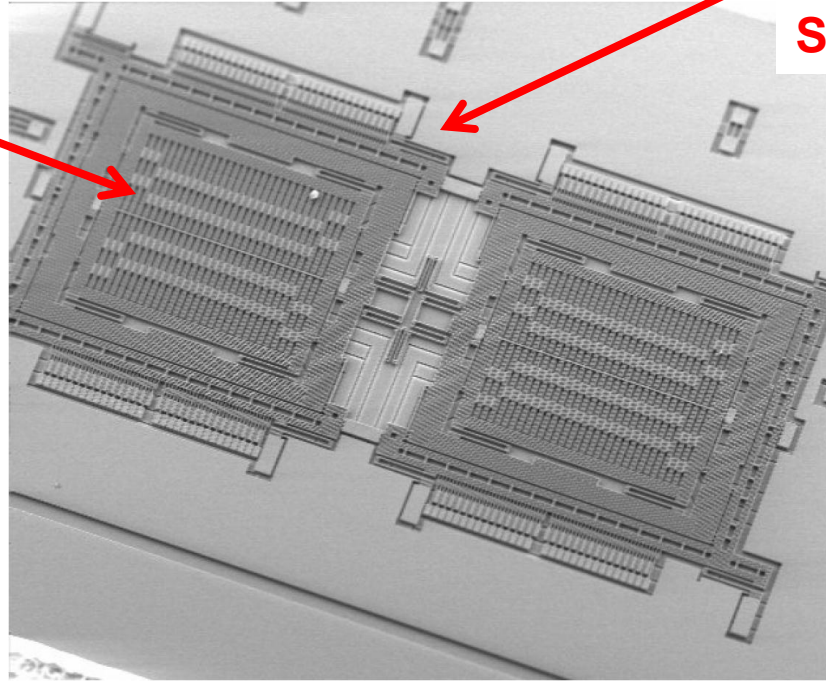
- Vibrate along drive axis with oscillator @ f_{drive}

- Detect vibration @ f_{drive} about sense axis with accelerometer

$$x \cong \frac{1}{4000} \text{ Angstrom}$$

Gyroscope Design

Electrostatic
Drive

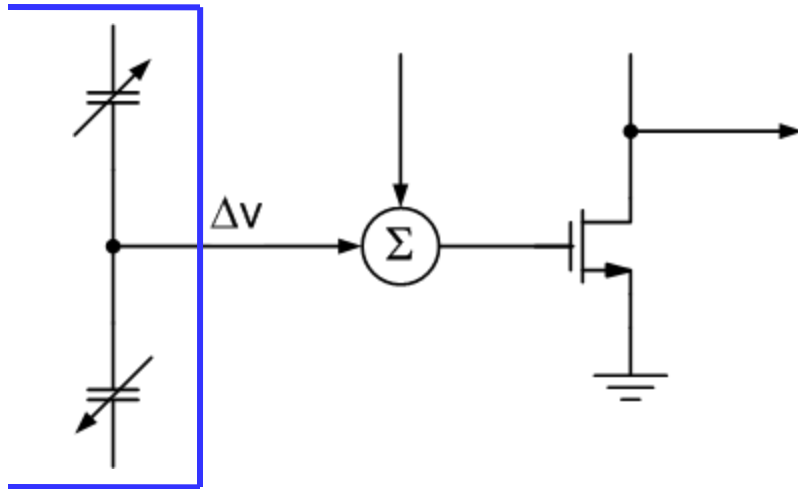


Electrostatic
Sense Pickup

Power / Accuracy Tradeoff

$$\underbrace{\overline{v_n^2} \propto \frac{1}{g_m} \propto \frac{1}{I_D}}$$

$$SNR = \frac{\text{signal}}{\text{noise}} = \text{const}$$



gyro

Design options:

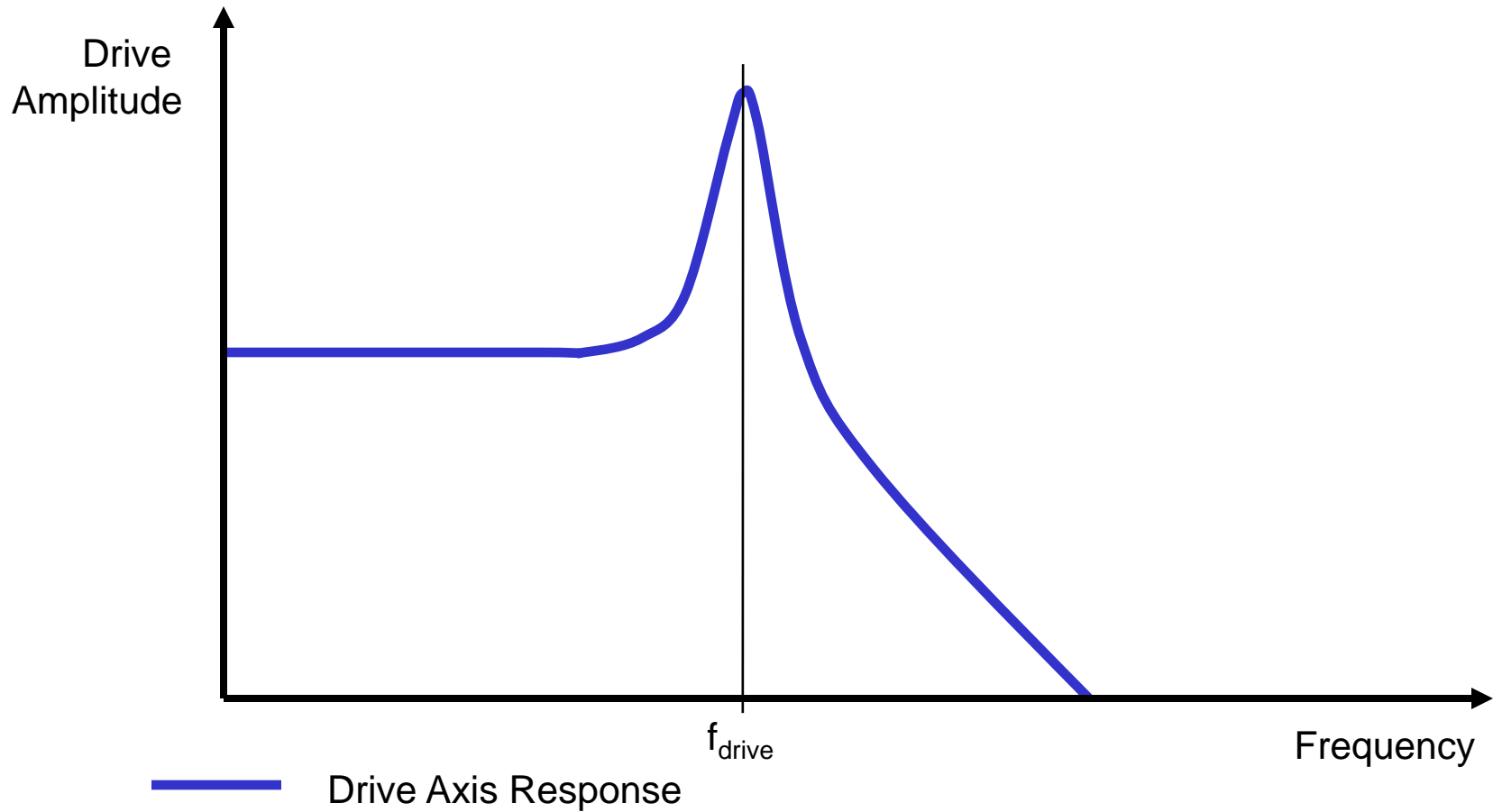
- 1) Lower amplifier noise
- 2) Increase signal Δv

without power penalty

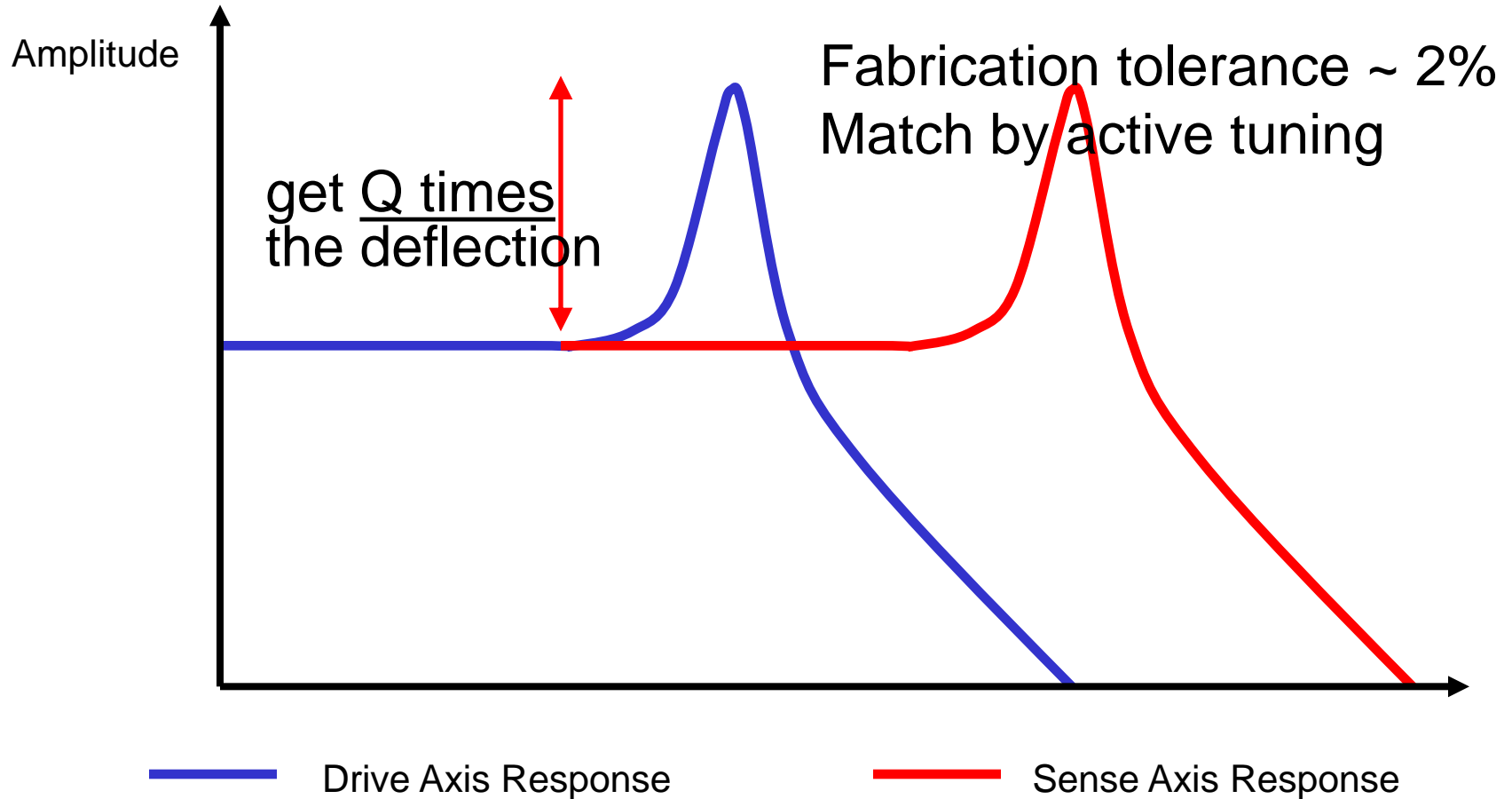
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Mode-Matching

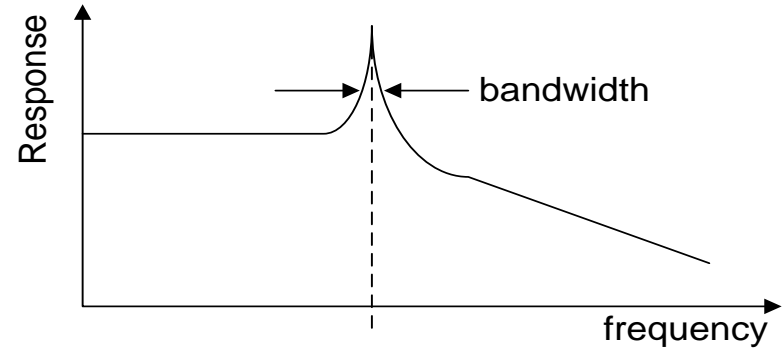
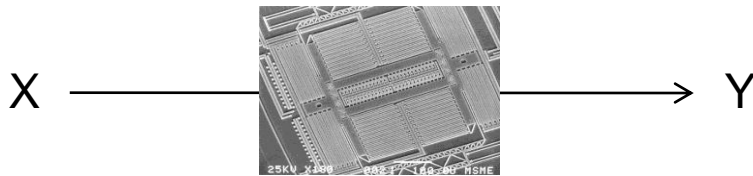


Mode-Matching

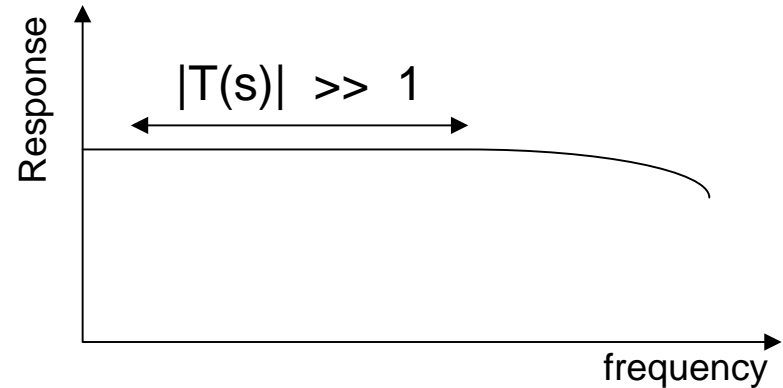
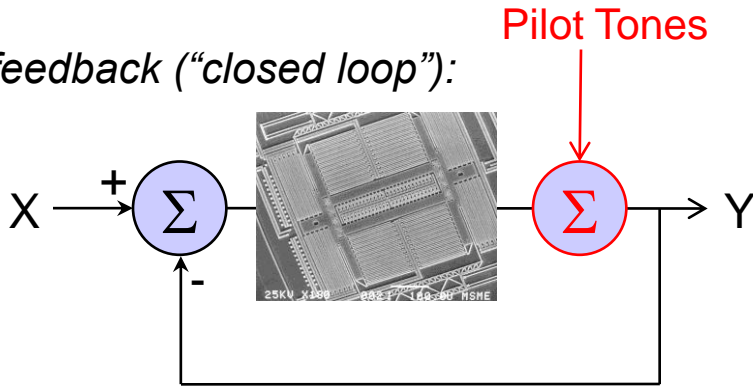


Frequency Error Estimation

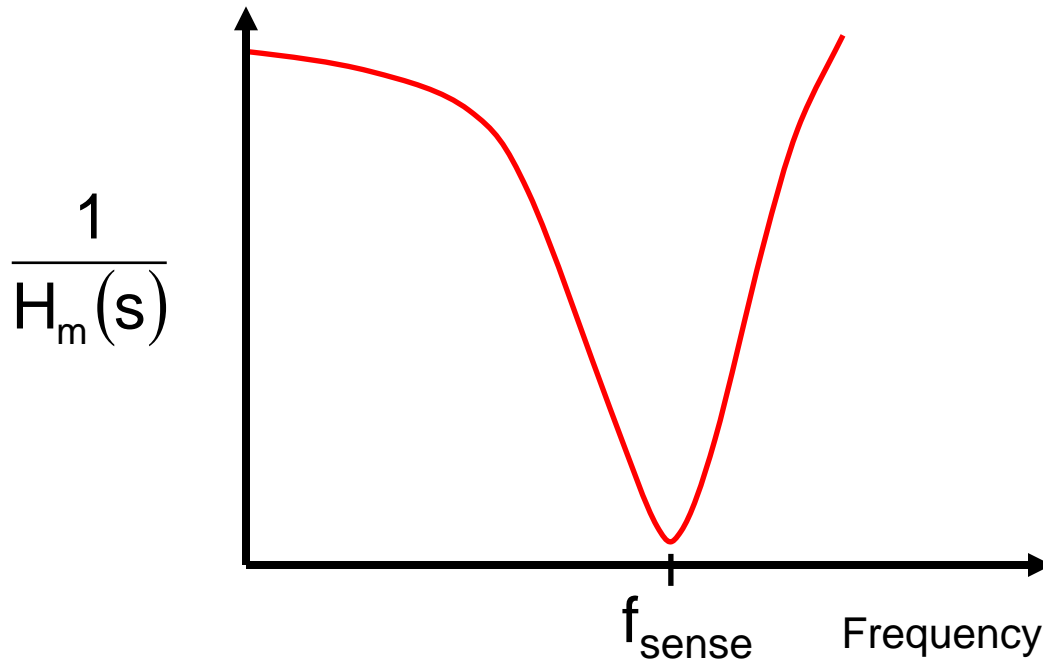
open loop response:



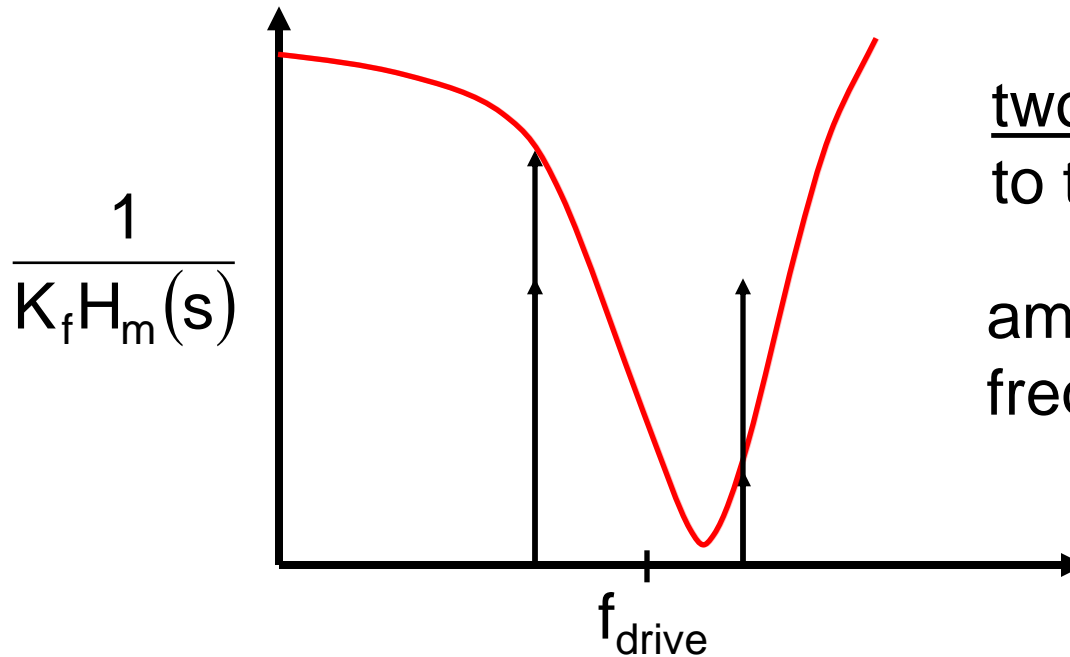
feedback ("closed loop"):



Sense Resonance Estimation



Key Idea

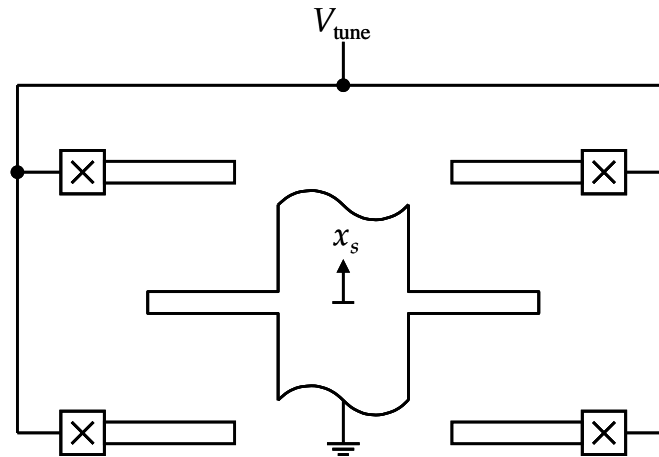


two pilot tones locked to the drive frequency

amplitudes depend on frequency mismatch!

force amplitude difference to zero

Electrostatic Tuning

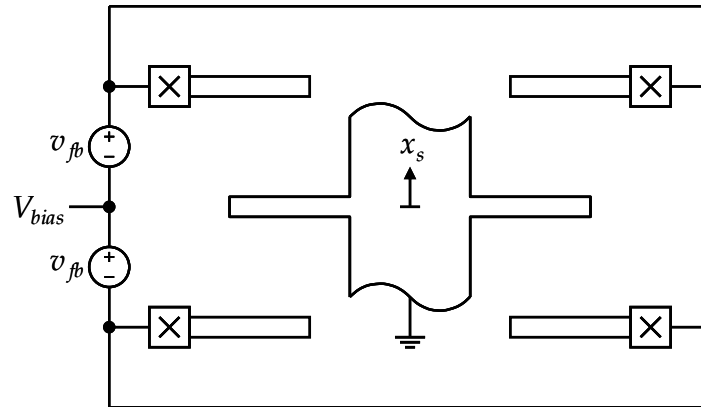


Voltage-Tunable Spring

Net Stiffness $\longrightarrow k_s = k_m - \underbrace{\frac{C_{\text{tune}}}{\text{gap}^2} V_{\text{tune}}^2}_{\text{Electrostatic Spring}}$

\nearrow
 Mechanical

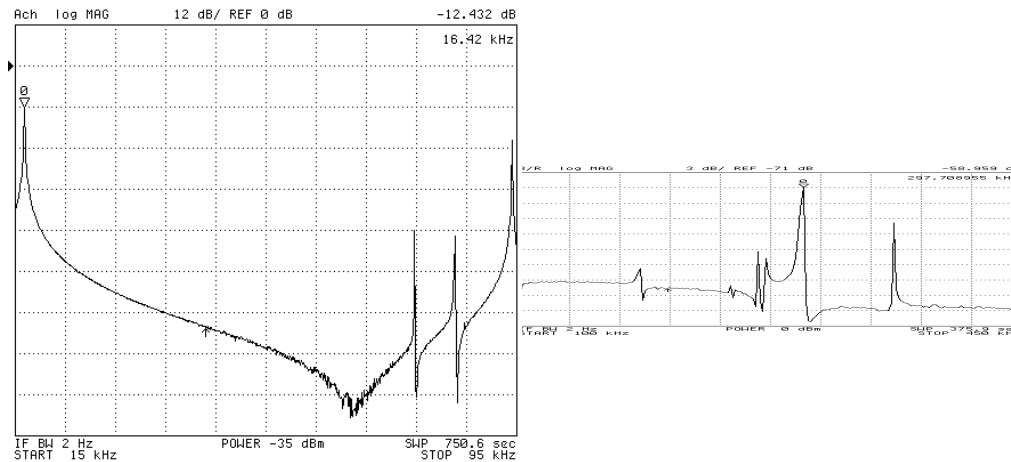
Electrostatic Force Feedback



$$F_e = \underbrace{2 \frac{C_{s0}}{\text{gap}} V_{bias}}_{\text{Voltage-To-Force Gain}} v_{fb} - \underbrace{2 \frac{C_{s0}}{\text{gap}^2} (V_{bias}^2 + v_{fb}^2)}_{\text{Signal Dependent Stiffness}} x_s$$

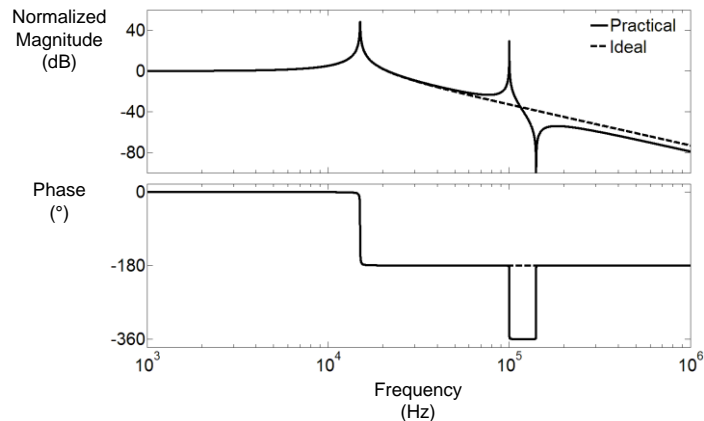
2-level feedback (Sampled Data “ $\Sigma\Delta$ ”)

Sensor Frequency Response

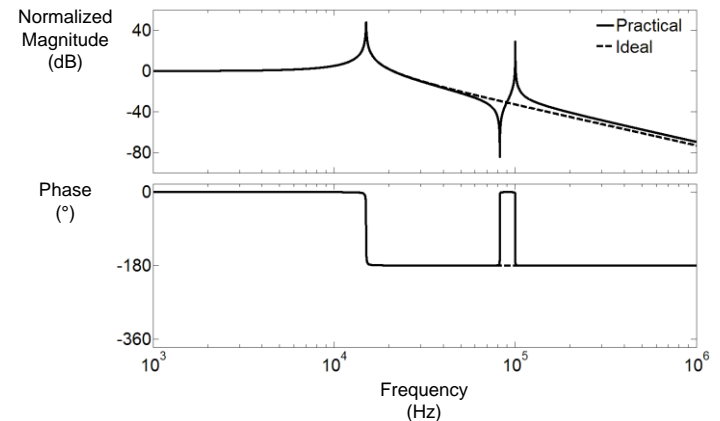


- Main mode near 15kHz
- Big parasitic modes near 95kHz and 300kHz
- Smaller parasitic modes all over
- Feedback?

Parasitic Resonances

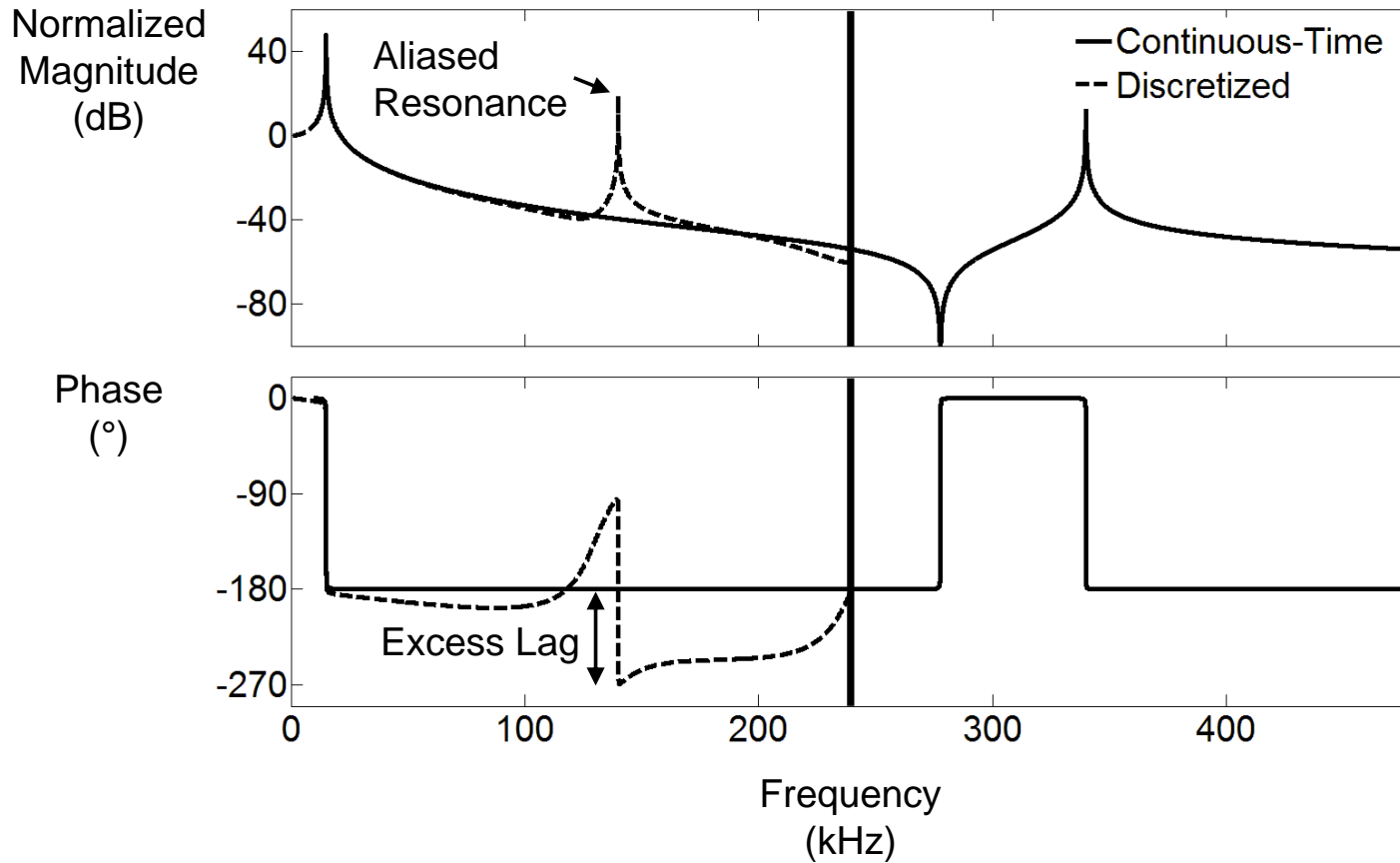


Non-collocated Control
(separate electrodes)

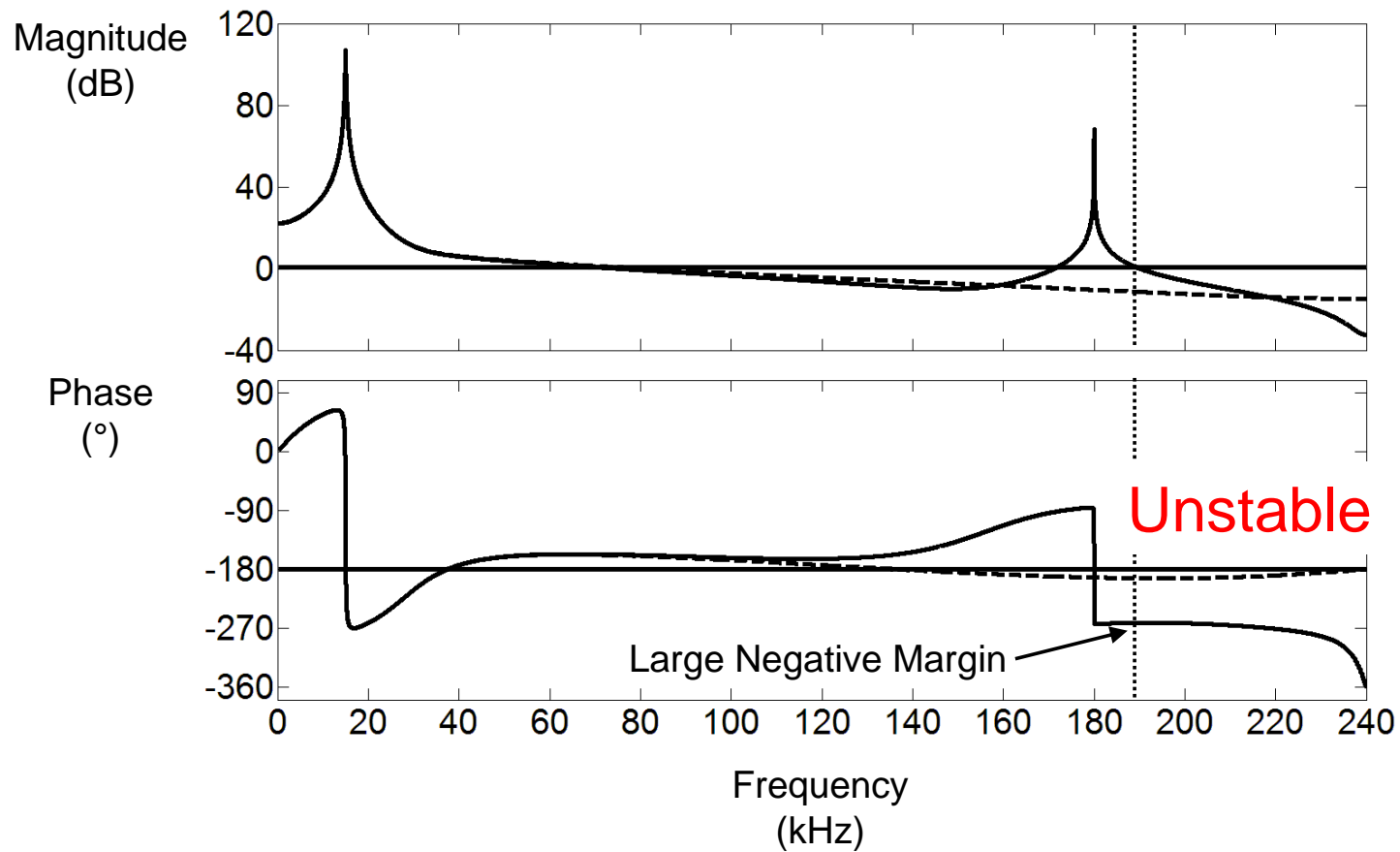


Collocated Control
(same electrode)

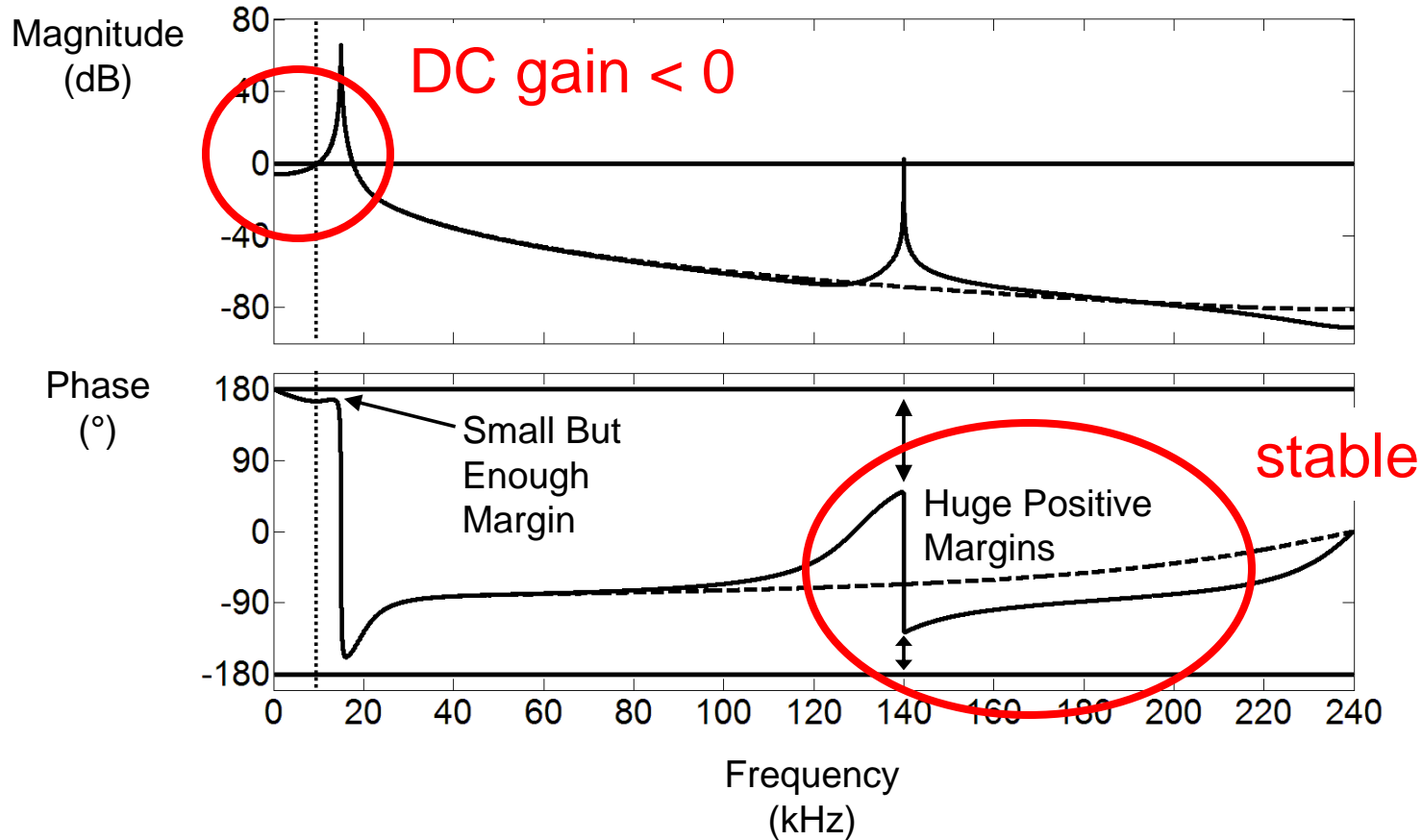
Sampled Data System



Negative Feedback



Positive Feedback



Mode-Matching Summary

👉 >100x increased signal

👉 ***100x power savings***

👉 Fabrication tolerances, drift → mismatch

➤ Background calibration

➤ Electrostatic tuning

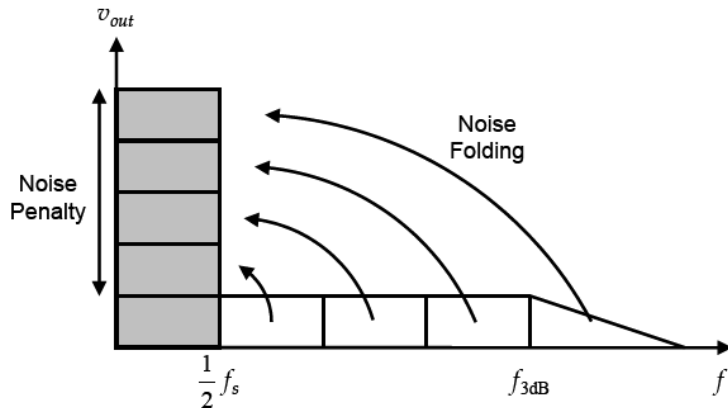
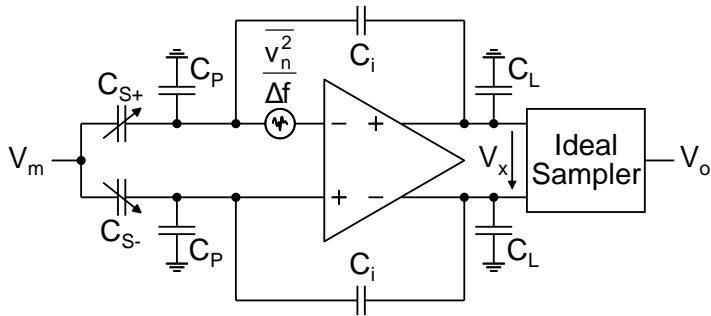
👉 Sensitivity = $f(Q, \text{environment})$

➤ Force feedback

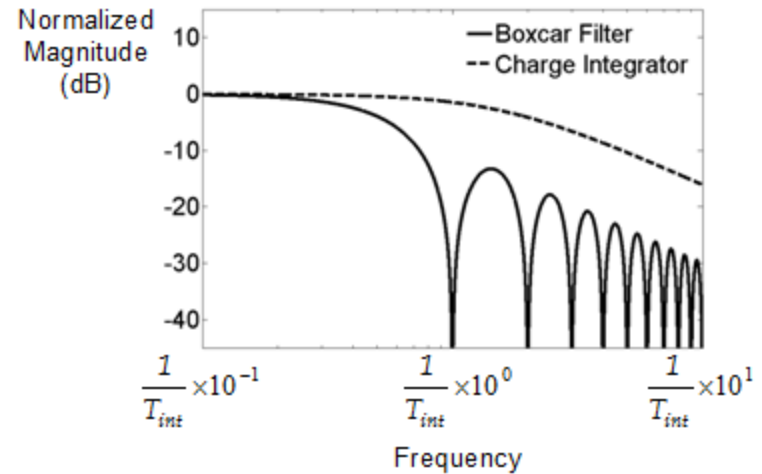
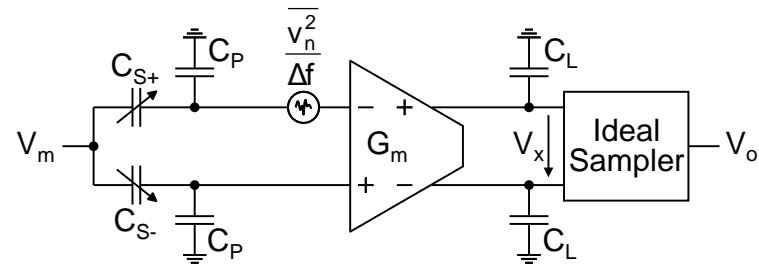
➤ Stability → positive feedback

Sampling Noise

Closed Loop



Open Loop

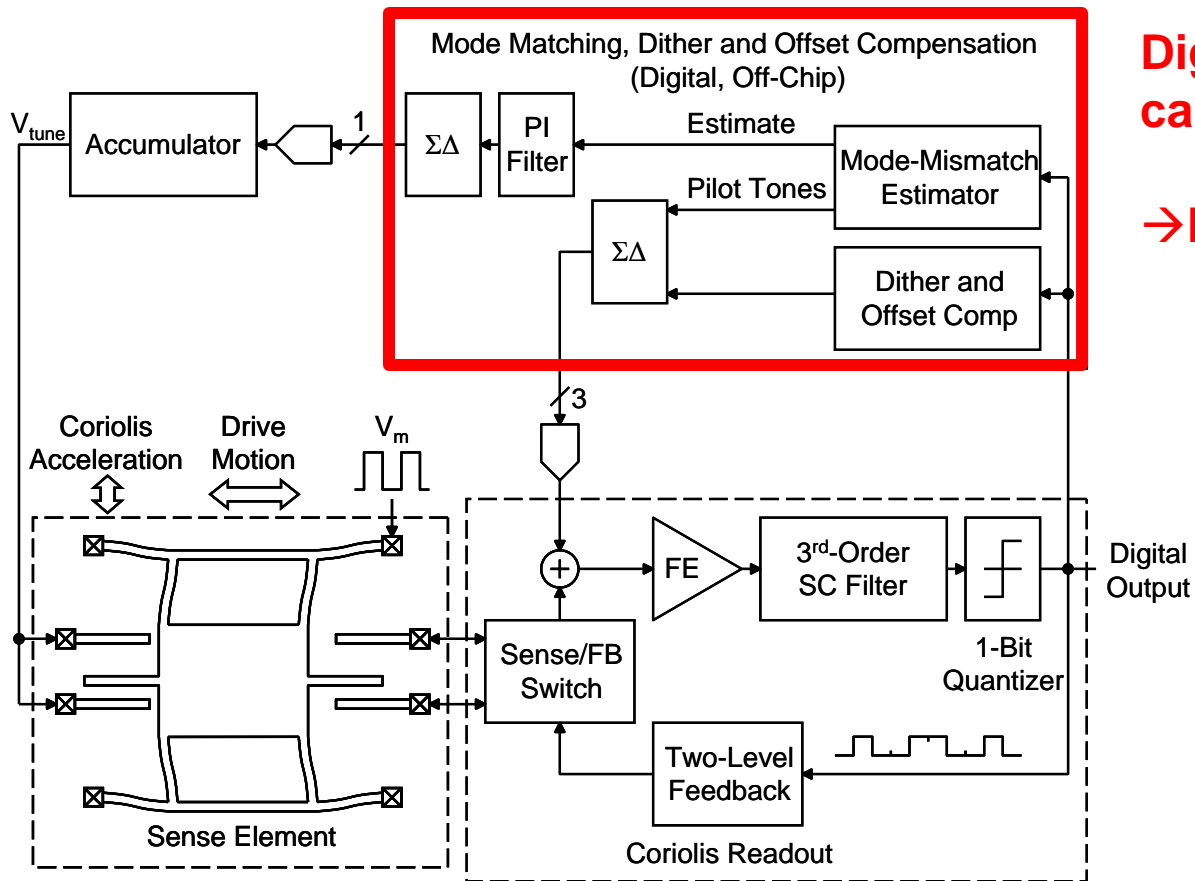


Boxcar Sampler versus Charge Integrator

$$\frac{\text{SNR}_{\text{BS}}}{\text{SNR}_{\text{CI}}} = \underbrace{\left(\frac{1}{1-F} \right)^2}_{\text{feedback penalty}} \underbrace{\left(\frac{n_\tau}{2} \right)}_{\text{settling penalty}}$$

- $n_\tau = T_s / \tau_{\text{amp}}$ of charge integrator
- F = feedback factor of charge integrator
- Typical SNR improvement $\sim 10\text{dB}$
- ***10x power savings!***

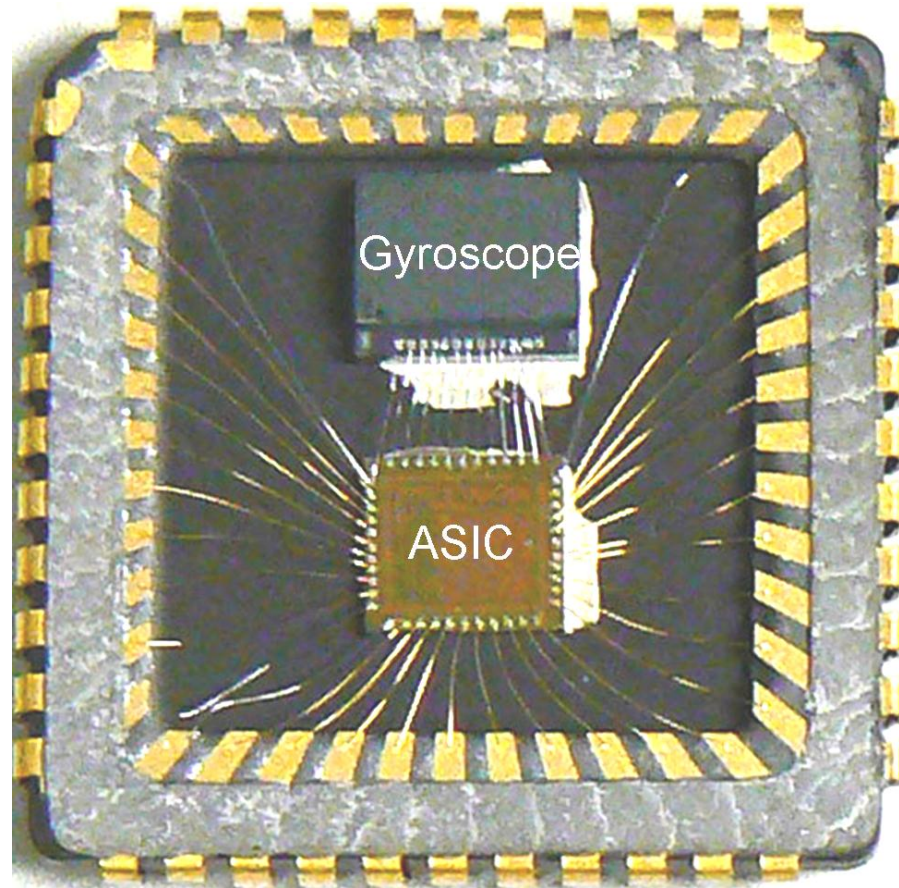
System Block Diagram



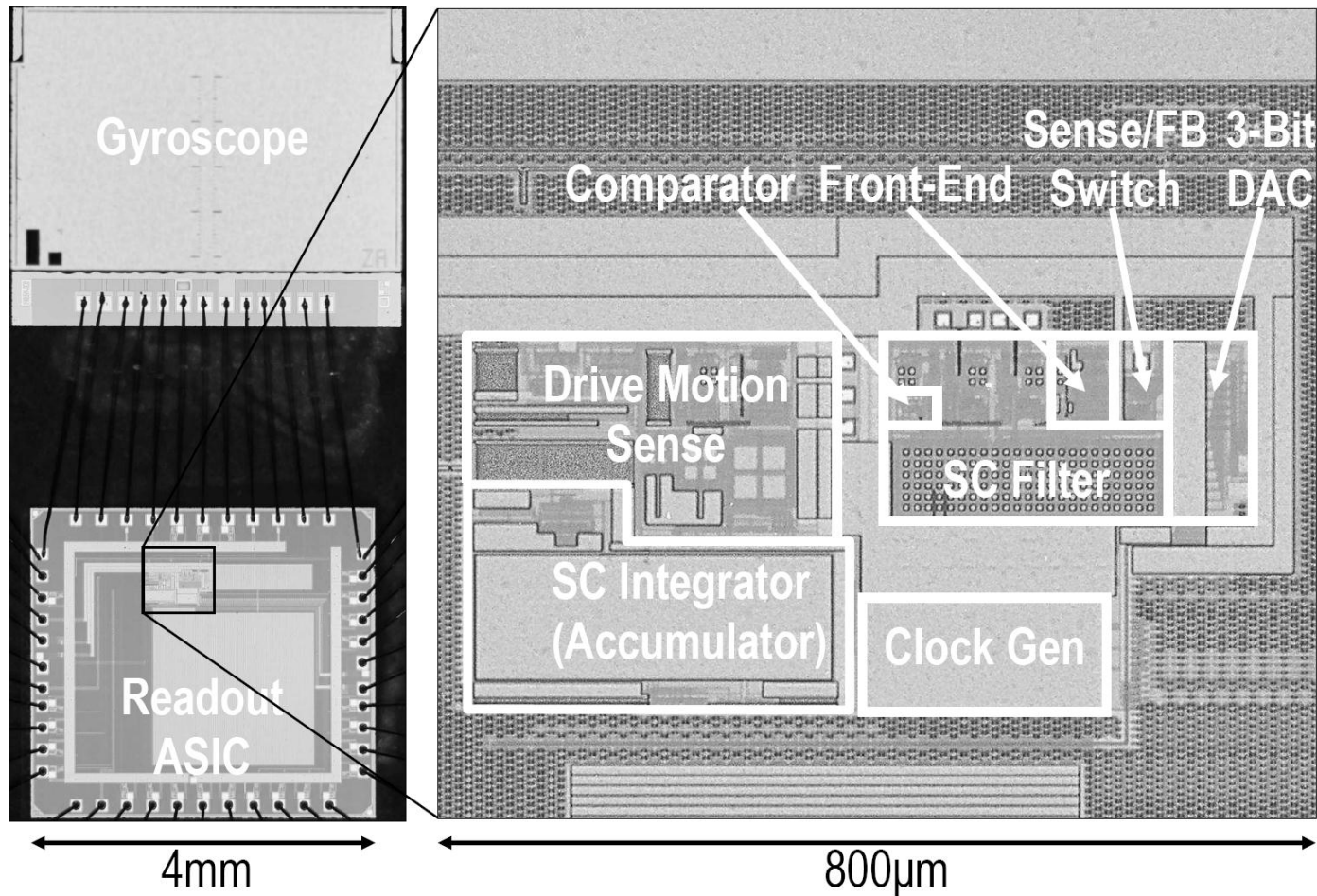
Digital background calibration

→ Negligible power penalty

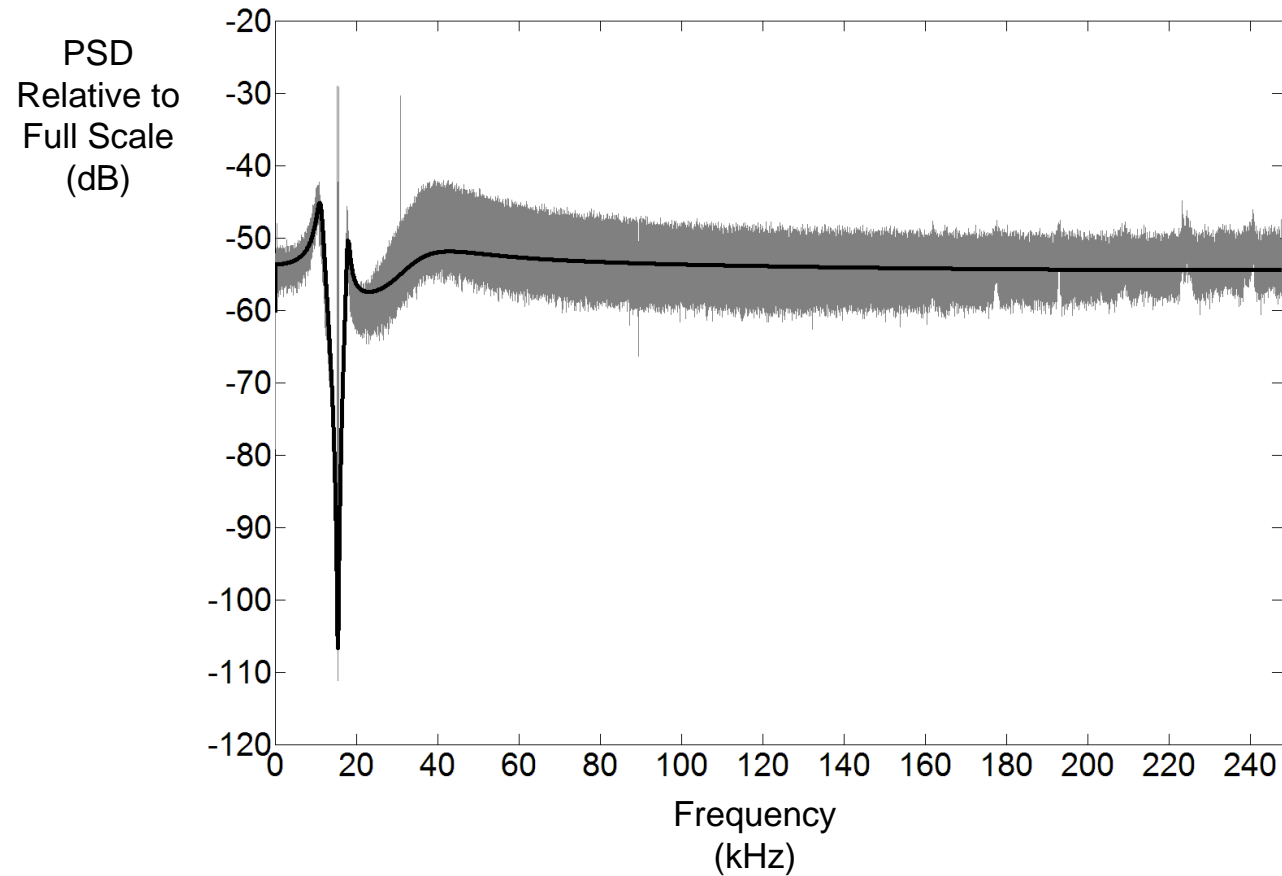
Chip Photo



Chip Micrograph



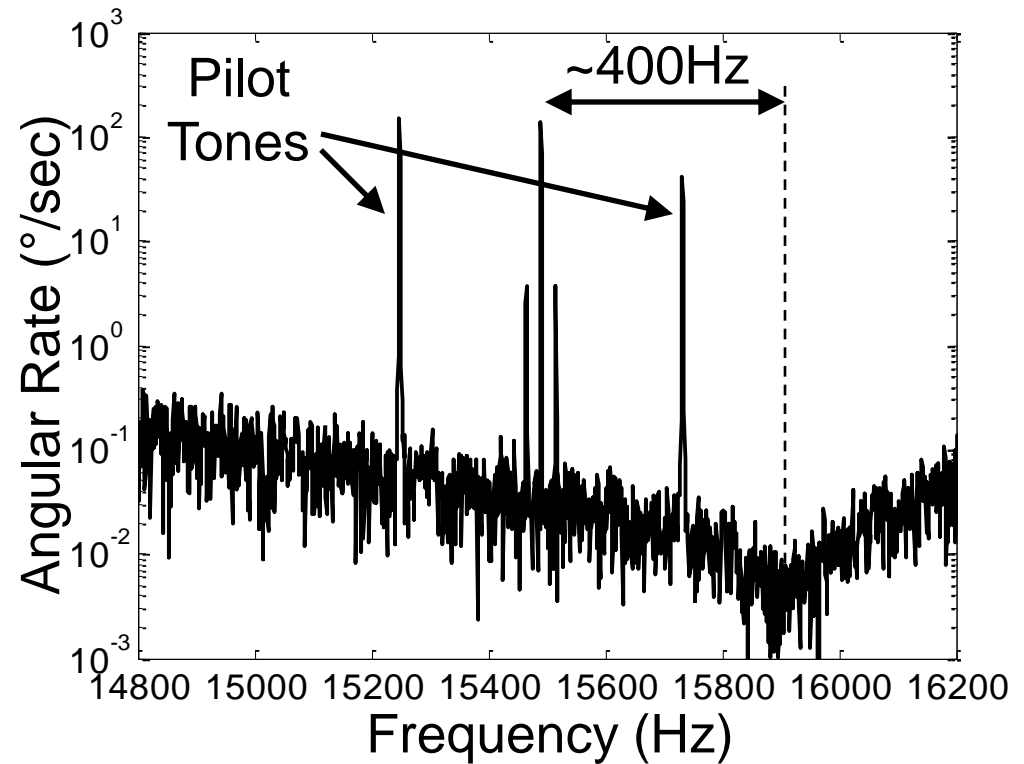
Output Spectrum



Output Spectrum

Without calibration

- Noise Floor:
 $0.03^\circ/\text{s}/\sqrt{\text{Hz}}$
- Mismatch:
 $\sim 400\text{Hz}$ (2.6%)



Output Spectrum

Without calibration

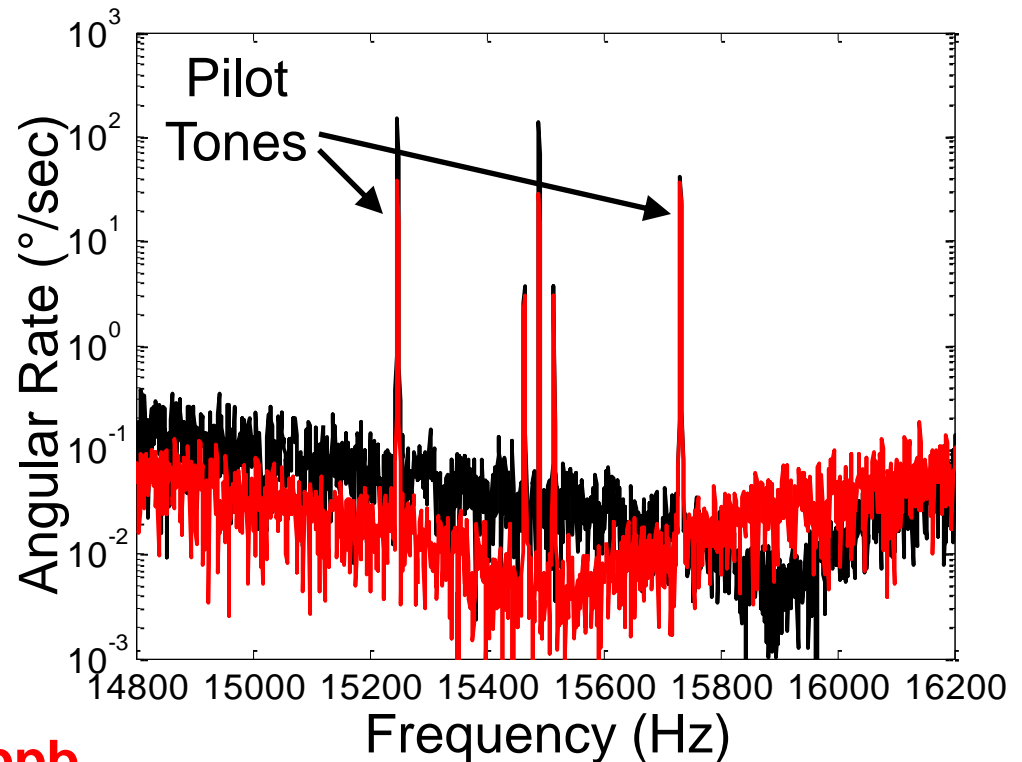
- Noise Floor:
 $0.03^\circ/\text{s}/\sqrt{\text{Hz}}$
- Mismatch:
 $\sim 400\text{Hz}$ (2.6%)

With Calibration

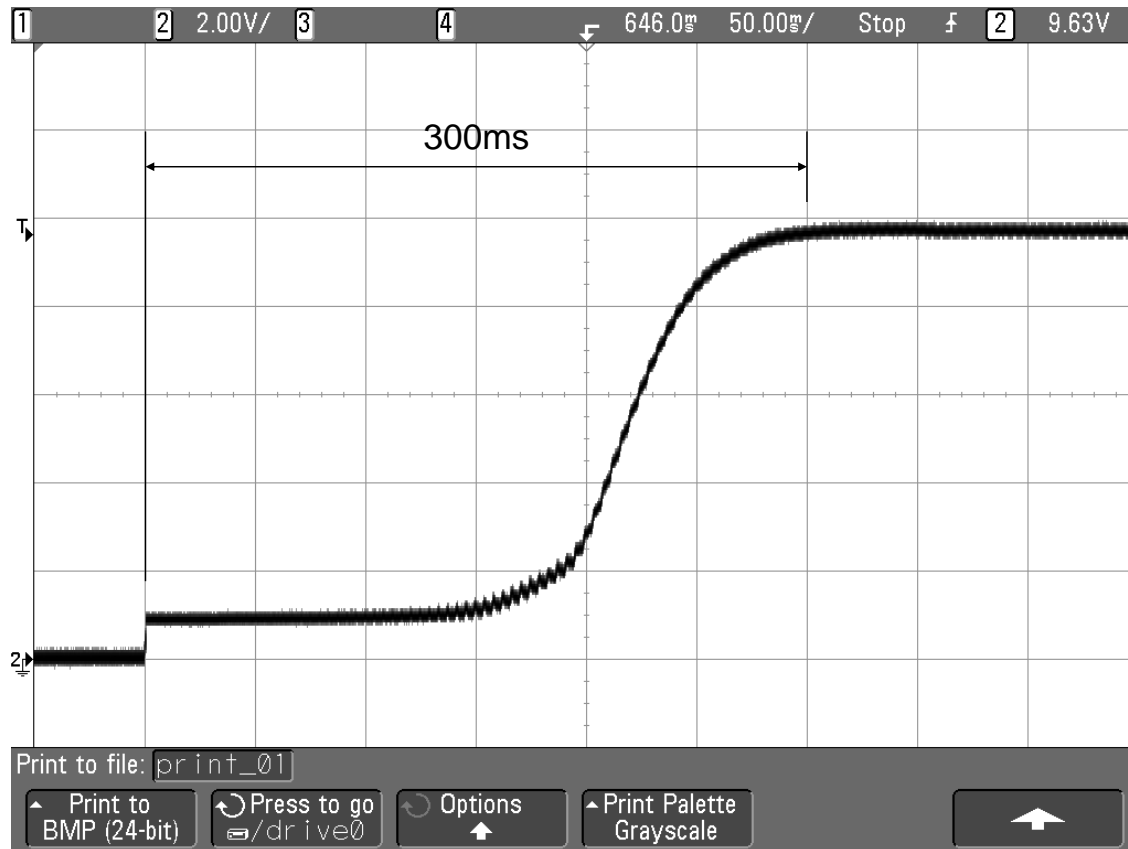
- **Noise Floor:**
 $0.004^\circ/\text{s}/\sqrt{\text{Hz}}$
- **Mismatch:**
 $\ll 50\text{Hz}$ (0.3%)

Capacitance resolution

- **1Hz bandwidth**
 $0.3\text{aF}/12.5\text{pF} = 24\text{ppb}$



Tuning Voltage Startup Transient



Results Summary

- Power dissipation:
1mW (excluding drive)
- Front-end power reduction:
 - Mode-matching: 100x
 - Boxcar sampling: 10x
- 1000x combined power savings!

Comparison to previous work

Reference	Power (mW)	Noise ($^{\circ}/\text{sec}/\sqrt{\text{Hz}}$)	BW (Hz)	Tuning Time (sec)
[1]	30	0.05	20	-
[2]	13	1	40	-
[3]	31	0.05	36	-
[4]	6	-	0.2	140
This work	1	0.004	50	0.3

[1] Geen, JSSC 2002

[2] Petkov, ISSCC 2004

[3] Saukoski, ESSCIRC 2006

[4] Sharma, ISSCC 2007

Conclusions

- Power savings
 - Mechanical gain → 100x reduction
 - Open-loop charge amplifier → 10x reduction
 - Digital processing occurs minimum power overhead
- Techniques
 - Background calibrated mode matching
 - insensitive to process variations
 - Positive feedback
 - insensitive to parasitic modes

Acknowledgements

- Christoph Lang & Vladimir Petkov
- Robert Bosch Corporation
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